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veneer subroutine with a return to native instruction within said non-native veneer subroutine serving to return processing to said native code.

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8. (Amended) Apparatus as claimed in claim 6, wherein said non-native veneer subroutine is dynamically created when said non-native subroutine is called from native code.

A B
10. (Amended) Apparatus as claimed in claim 1, wherein said instruction translator is responsive to a plurality of types of return to non-native instruction.

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13. (Amended) Apparatus as claimed in claim 1, wherein said instruction translator is responsive to a plurality of types of return to native instruction.

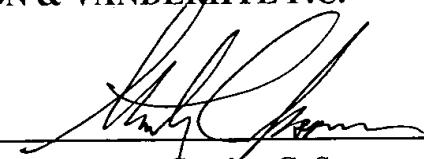
REMARKS

The above amendments are made to place the claims in a more traditional format.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "**Version With Markings To Show Changes Made.**"

Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS

5. (Amended) Apparatus as claimed in [any one of the preceding claims 1] claim 1, wherein said non-native instructions are Java Virtual Machine instructions.

6. (Amended) Apparatus as claimed in [any one of the preceding claims] claim 1, wherein a non-native subroutine is called from native code via a non-native veneer subroutine, such that, upon completion of said non-native subroutine, a return to non-native instruction can be used to return processing to said non-native veneer subroutine with a return to native instruction within said non-native veneer subroutine serving to return processing to said native code.

8. (Amended) Apparatus as claimed in [any one of claims 6 and 7] claim 6, wherein said non-native veneer subroutine is dynamically created when said non-native subroutine is called from native code.

10. (Amended) Apparatus as claimed in [any one of the preceding claims] claim 1, wherein said instruction translator is responsive to a plurality of types of return to non-native instruction.

13. (Amended) Apparatus as claimed in [any one of the preceding claims] claim 1, wherein said instruction translator is responsive to a plurality of types of return to native instruction.